

Description
Feature

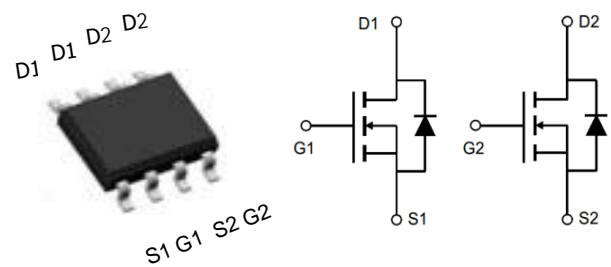
- ◇ High Speed Power Switching, logic level
- ◇ Enhanced Body diode dv/dt capability
- ◇ Enhanced Avalanche Ruggedness
- ◇ Lead Free

Application

- ◇ Synchronous Rectification in SMPS
- ◇ Hard Switching and High Speed Circuit
- ◇ Power Tools
- ◇ UPS
- ◇ Motor Control

Product Summary

BVDSS	RDSON	ID
40V	12mΩ	12A

SOP8 Pin Configuration

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
AGM408M	AGM408M	SOP8	325mm	16mm	3000

Table 1. Absolute Maximum Ratings (TA=25°C)

Symbol	Parameter	Value	Unit
V _{DS}	Drain-Source Voltage (V _{GS} =0V)	40	V
V _{GS}	Gate-Source Voltage (V _{DS} =0V)	±20	V
I _D	Drain Current-Continuous(T _c =25°C) (Note 1)	12	A
	Drain Current-Continuous(T _c =100°C)	6.8	A
I _{DM (pulse)}	Drain Current-Continuous@ Current-Pulsed (Note 2)	32	A
P _D	Maximum Power Dissipation(T _c =25°C)	2	W
	Maximum Power Dissipation(T _c =100°C)	1.5	W
E _{AS}	Avalanche energy (Note 3)	25	mJ
T _J , T _{STG}	Operating Junction and Storage Temperature Range	-55 To 150	°C

Table 2. Thermal Characteristic

Symbol	Parameter	Typ	Max	Unit
R _{θJA}	Thermal Resistance Junction-ambient (Steady State) ¹	---	62	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	4.5	°C/W

Table 3. Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	40			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V			1	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V, V _{DS} =0V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.2	1.6	2.5	V
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =8A		33		S
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _D =8A		12	16	mΩ
		V _{GS} =4.5V, I _D =5A		18.9	26	mΩ
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =20V, V _{GS} =0V, F=1MHZ		961		pF
C _{oss}	Output Capacitance			108		pF
C _{rss}	Reverse Transfer Capacitance			96		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1.0MHz		1.7		Ω
Switching Times						
t _{d(on)}	Turn-on Delay Time	V _{GS} =10V, V _{DD} =20V, R _L =2.5Ω, R _{GEN} =3.3Ω		5.5		nS
t _r	Turn-on Rise Time			14		nS
t _{d(off)}	Turn-Off Delay Time			24		nS
t _f	Turn-Off Fall Time			12		nS
Q _g	Total Gate Charge	V _{DS} =20V, I _D =8A, V _{GS} =10V		22		nC
Q _{gs}	Gate-Source Charge			3.5		nC
Q _{gd}	Gate-Drain Charge			5.3		nC
Source-Drain Diode Characteristics						
I _{SD}	Source-Drain Current(Body Diode)				11	A
V _{SD}	Forward on Voltage	V _{GS} =0V, I _S =9A			1.2	V
t _{rr}	Reverse Recovery Time	I _F =-15A, dI/dt=100A/μs, ·T _J =25°C			19	ns
Q _{rr}	Reverse Recovery Charge					9

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: T_J=25°C, V_{DD}=15V, V_G=10V, R_G=25Ω

N- Channel Typical Electrical and Thermal Characteristics (Curves)

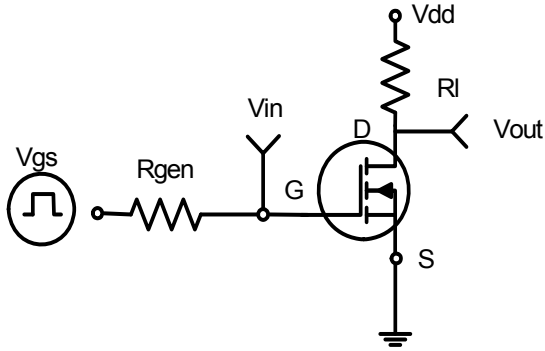


Figure 1: Switching Test Circuit

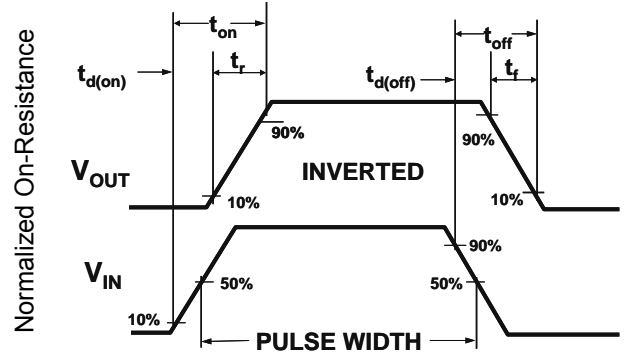


Figure 2: Switching Waveforms

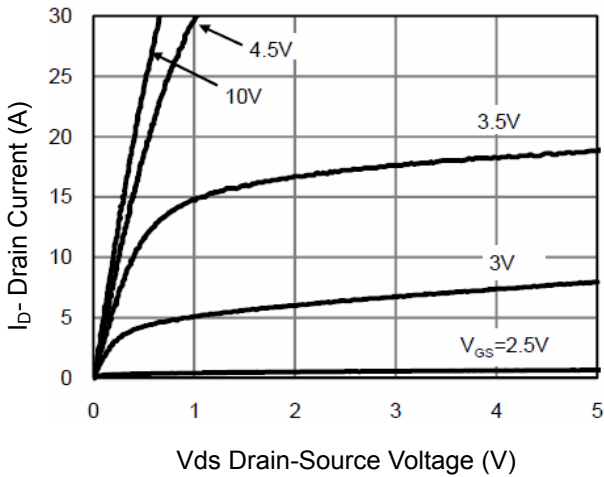


Figure 3 Output Characteristics

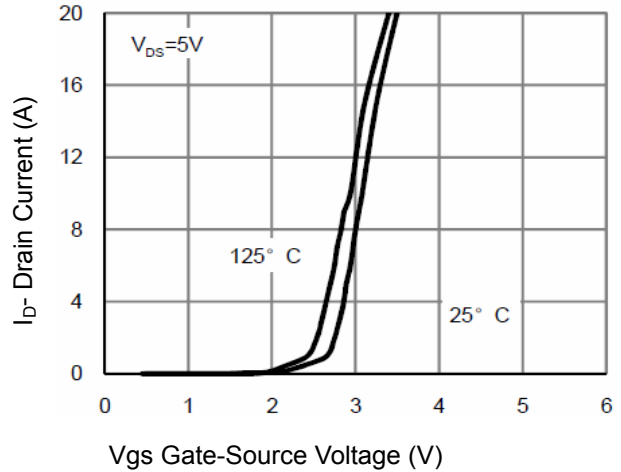


Figure 4 Transfer Characteristics

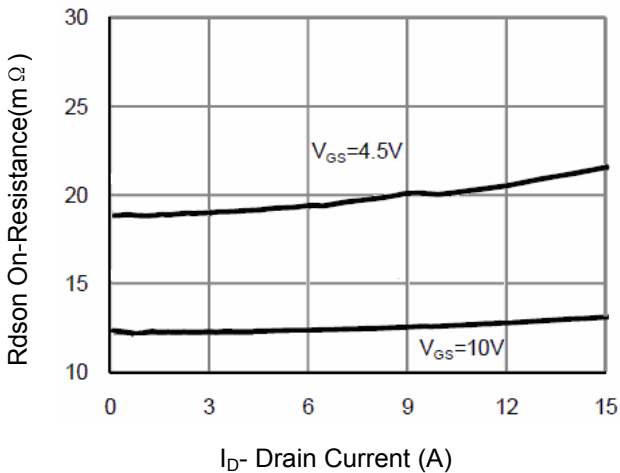


Figure 5 Drain-Source On-Resistance

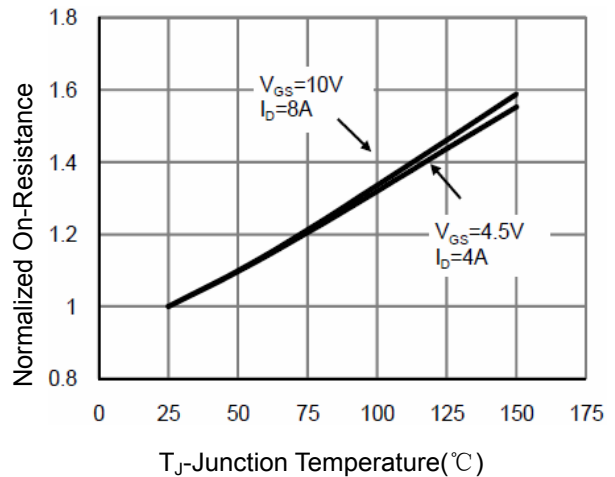


Figure 6 Drain-Source On-Resistance

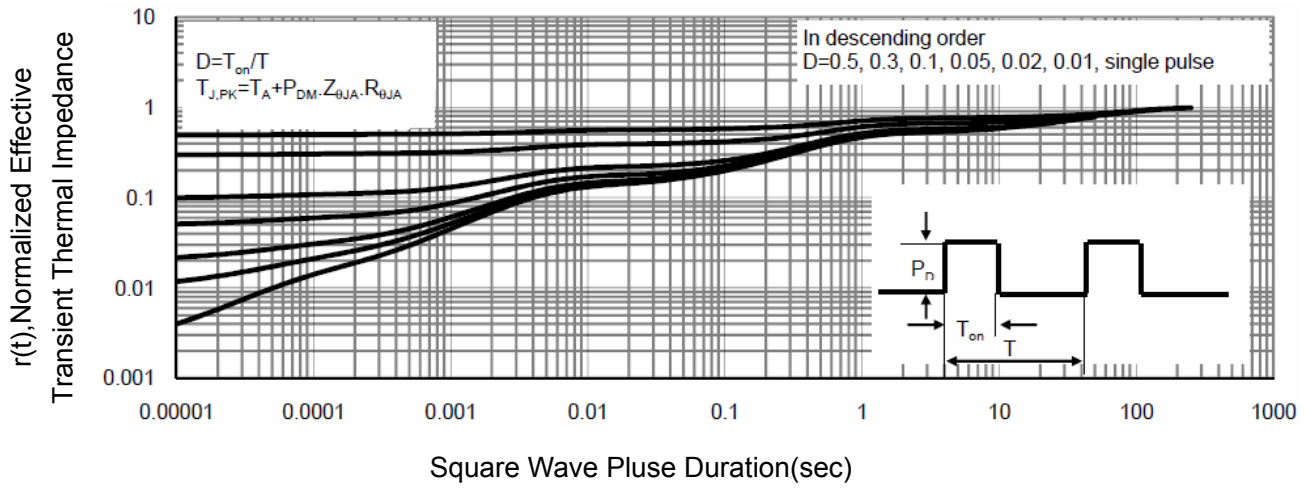
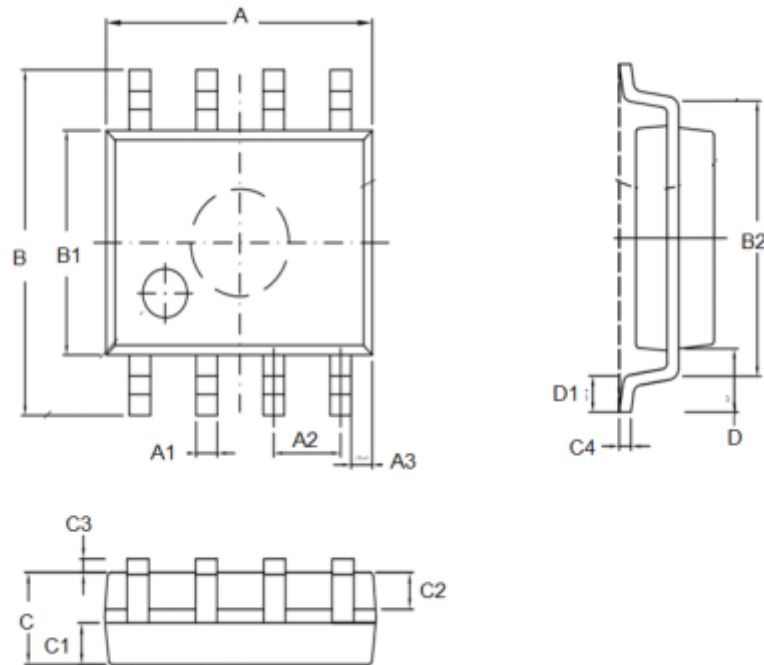


Figure 13 Normalized Maximum Transient Thermal Impedance

●Dimensions(SOP8)

SYMBOL	min	TYP	max	SYMBOL	min		max
A	4.80		5.00	C	1.30		1.50
A1	0.37		0.47	C1	0.55		0.75
A2		1.27		C2	0.55		0.65
A3		0.41		C3	0.05		0.20
B	5.80		6.20	C4	0.19	0.20	0.23
B1	3.80		4.00	D		1.05	
B2		5.00		D1	0.40		0.62




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